Docket No.: 60188-648 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Customer Number: 20277

Makoto FUJIWARA, et al.

Confirmation Number: 4108

Serial No.: 10/654,084

Group Art Unit: 2131

Filed: September 04, 2003

Examiner: Unknown

For:

SEMICONDUCTOR DEVICE INCLUDING ENCRYPTION SECTION,

SEMICONDUCTOR DEVICE INCLUDING EXTERNAL INTERFACE, AND

CONTENT REPRODUCTION METHOD

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the name of the Assignee is spelled incorrect. Attached is a copy of the Assignment Document, which evidences the Assignee information should read: Matsushita Electric Industrial Co., Ltd.. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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Date: January 14, 2004



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Jack Q. Lever, Jr. McDERMOTT, WILL & EMERY 600 Thirteenth Street, N.W. Washington, DC 20005-3096 JAN 1 4 2004 BERTHAMARIA OF THE ADEMARK OF THE ADEM

CONFIRMATION NO. 4108

FILING RECEIPT

OC000000011505812

Date Mailed: 12/16/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Makoto Fujiwara, Kyoto, JAPAN; Yusuke Nemoto, Hyogo, JAPAN; Junichi Yasui, Osaka, JAPAN; Takuji Maeda, Osaka, JAPAN; Takayuki Ito, Osaka, JAPAN; Yasushi Yamada, Aichi, JAPAN; Shinji Inoue, Osaka, JAPAN;

Assignment For Published Patent Application

Mitsushita Electric Industrial Co., Ltd.;

MATSUSHITM

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-258481 09/04/2002

If Required, Foreign Filing License Granted: 12/16/2003

Projected Publication Date: 03/25/2004

Non-Publication Request: No

Early Publication Request: No



Title

Semiconductor device including encryption section, semiconductor device including external interface, and content reproduction method

Preliminary Class

380

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